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**TRANSMITTAL LETTER**  
**(General - Patent Issued)**Docket No.  
**FIS920030051US1**Patentee/Owner  
**Steegen et al.**

U.S. Patent No.

**6,936,522 B2**

Issue Date

**August 30, 2005**Title: **SELECTIVE SILICON-ON-INSULATOR ISOLATION STRUCTURE AND METHOD**COMMISSIONER FOR PATENTS:

Transmitted herewith is:

**Certificate of Correction**  
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SignatureDated: **11-28-2005****Jack P. Friedman, Ph.D.<sup>®</sup>**  
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**(518) 220-1850**I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR 1.8(a)] on  
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# UNITED STATES PATENT AND TRADEMARK OFFICE

## CERTIFICATE OF CORRECTION

PATENT NO. : 6,936,522 B2

DATED : August 30, 2005

INVENTOR(S) : Steegen et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 3

Line 30, delete "Ã thick" and insert -- Åthick --

Column 6

Line 2, delete "700°0 C." and insert -- 700° C. --

Lines 61 and 62, delete "Å thick" and insert --Åthick--

Column 9

Line 44, delete "(c)" and insert --(e)--

Column 10

Line 1, delete "front" and insert --from--

Line 20, delete "method claim" and insert --method of claim --

MAILING ADDRESS OF SENDER (Please do not use customer number)

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